

REMARKS/ARGUMENTS

The foregoing amendment and the following arguments are provided to impart precision to the claims, by more particularly pointing out the invention, rather than to avoid prior art.

35 U.S.C. § 102(e) Rejections

Examiner rejected claims 1-20 under 35 U.S.C. § 102(e) as being unpatentable by U.S. Patent No. 6,140,714 (hereinafter "Fujii").

Claim 1 includes a limitation of a first stage having a first output coupled to a first component of the digital processing system and a second output which is different from the first output, and a second stage associated with a second component of the digital processing system, said second stage coupled to said first stage, wherein said first stage drives said second stage using the second output. Fujii does not include such a limitation and therefore does not anticipate claim 1. Fujii teaches where a primary power-supply circuit 11 has a single output coupled to both a secondary power-supply circuit 12 and a first driven circuit 21 (See Fig. 1). Therefore, both the secondary power-supply circuit and the first driven circuit are driven by the same output signal having the same voltage. Since Fujii does not teach a first stage driving a second stage using a second output which is different from the first output, Fujii does not anticipate claim 1.

New claim 21 includes a limitation of wherein said first output provides direct (DC) voltage, and wherein said second output provides an alternating

current (AC) voltage. As noted above, Fujii does not teach this limitation, as there is only a single output from the primary power-supply circuit. Therefore, Fujii does not anticipate new claim 21.

Claim 9 includes a limitation of a first circuit having a first output capable of providing power to a first component of the computer system and a second output which is different from the first output, and a second circuit capable of providing power to a second component of the computer system, wherein said first circuit drives the second circuit through the second output. Fujii does not teach such a limitation and therefore does not anticipate claim 9. Instead, as noted above, Fujii teaches a single output to power both a secondary power-supply circuit and a first drive circuit. As a result, claim 9 is not anticipated by Fujii.

Claim 22 includes a limitation of wherein said first output provides a direct current (DC) voltage and said second output provides an alternating current (AC) voltage. For the same reasons as mentioned with respect to claim 21, claim 22 is also not anticipated by Fujii.

Claim 15 includes a limitation of a power supply circuit coupled to a display device and a microprocessor of the computer system, wherein said power supply circuit is capable of supplying power to said display device and said microprocessor using at least two distinct power supply stages, and a main circuit coupled to said display device using a first output, and a secondary circuit coupled to said microprocessor, wherein said main circuit drives said secondary

circuit using a second output which is different from the first output. Fujii does not teach such a limitation and therefore does not anticipate claim 15. Instead, as above, Fujii teaches on a single output to power both a secondary power-supply circuit and a first drive circuit. As a result, claim 15 is not anticipated by Fujii.

Dependent claims 2-8, 11-14, and 17-20 depend from the above mentioned independent claims, and therefore include the limitations of the independent claims. Since the independent claims are not anticipated by Fujii, claims 2-8, 11-14 and 17-20 are also not anticipated by Fujii.

CONCLUSION

Applicants respectfully submit the present application is in condition for allowance. If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call Arlen M. Hartounian at (408) 720-8300.

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due.

Respectfully submitted,

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